



85V/120A N-Channel Advanced Power MOSFET

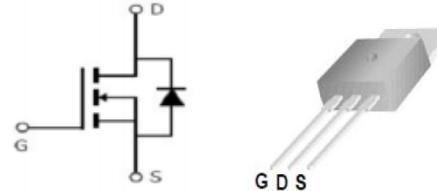
Features

- N-channel, normal level
- Very low on-resistance RDS(on)

BVDSS	85	V
ID	120	A
RDS(on)@VGS=10V	4.1	mΩ

Applications

- Industrial power supplies
- Boost converters
- Rectifier
- Telecom



TO-220

Order Information

Product	Package	Marking	Tube	Carton
PTP12HG08	TO-220	PTP12HG08	50PCS	5000PCS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
Common Ratings (TC=25°C Unless Otherwise Noted)			
V _{(BR)DSS}	Drain-Source Breakdown Voltage	85	V
V _{GS}	Gate-Source Voltage	±20	V
T _J	Maximum Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
I _S	Diode Continuous Forward Current	120	A
Mounted on Large Heat Sink			
E _{AS}	Single Pulse Avalanche Energy (Note1)	342	mJ
I _{DM}	Pulse Drain Current Tested (Silicon Limit) (Note2)	480	A
I _D	Continuous Drain current	120	A
P _D	Maximum Power Dissipation	176	W
R _{θJC}	Thermal Resistance Junction-to-Case (Note3)	0.71	°C/W
R _{θJA}	Thermal Resistance Junction-to-Ambient (Note3)	61	°C/W

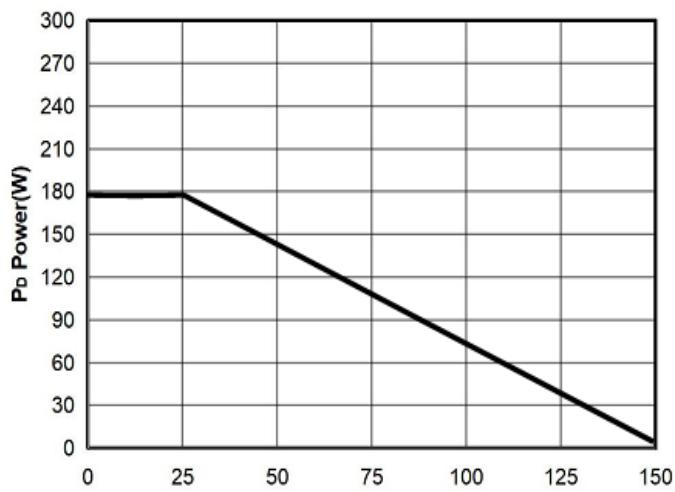
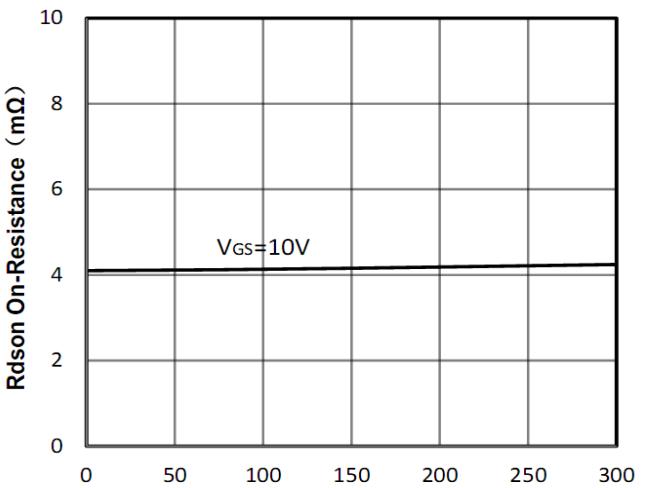
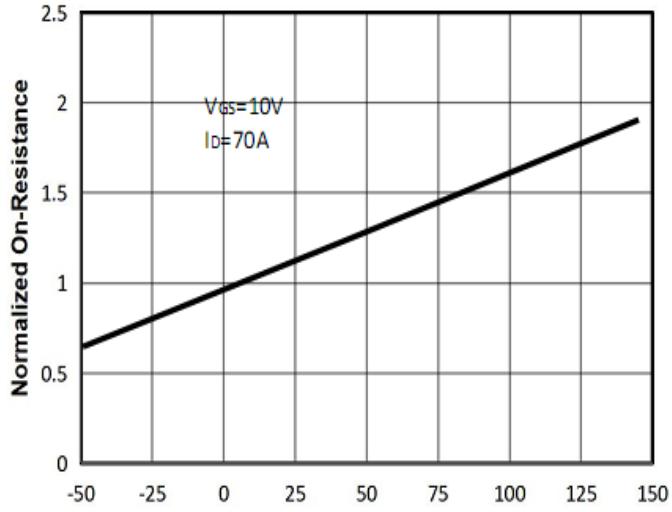
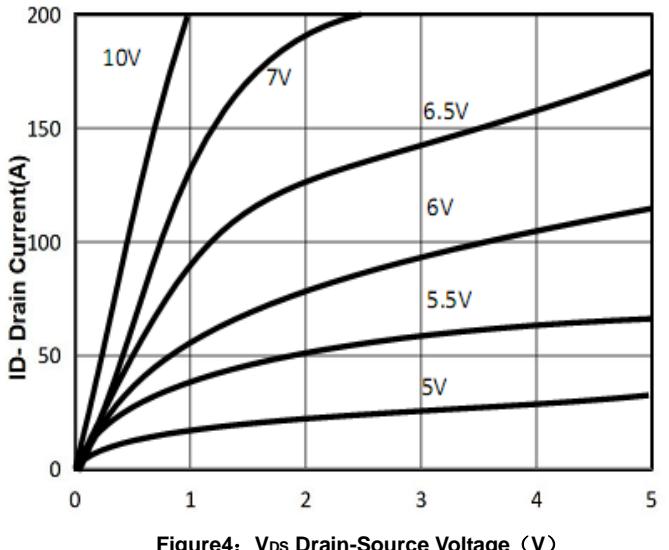
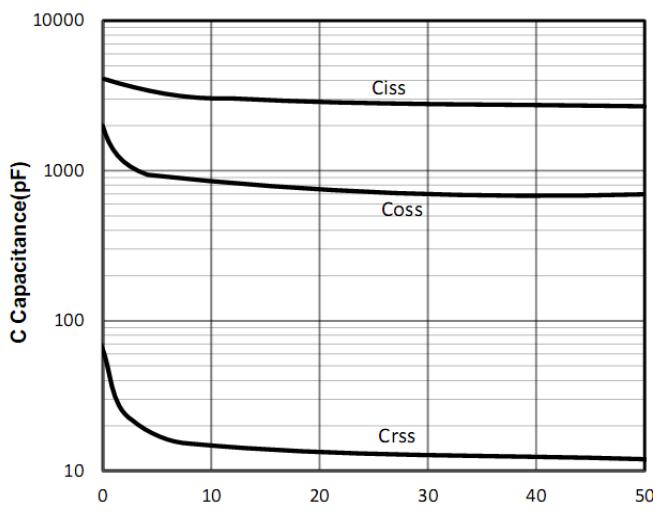
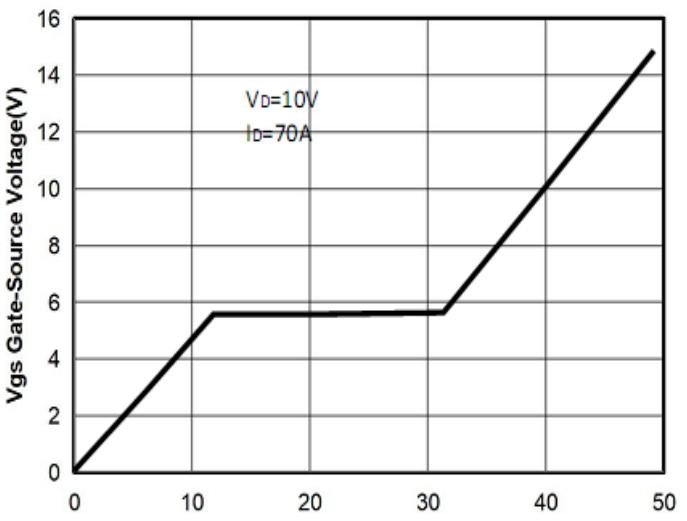


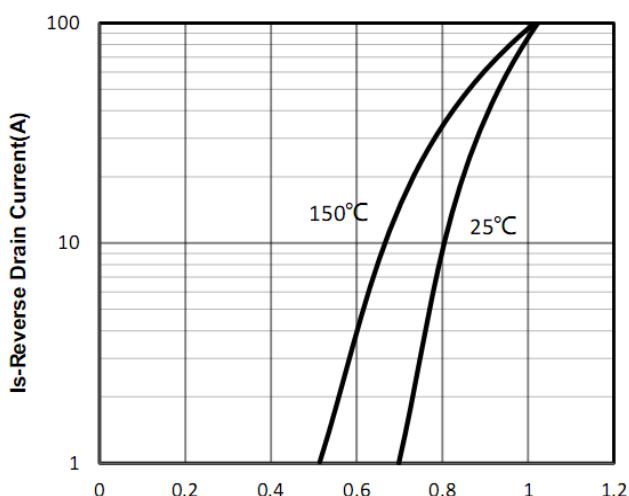
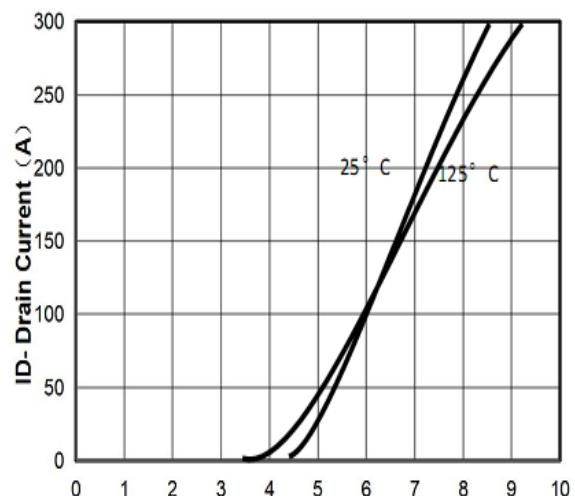
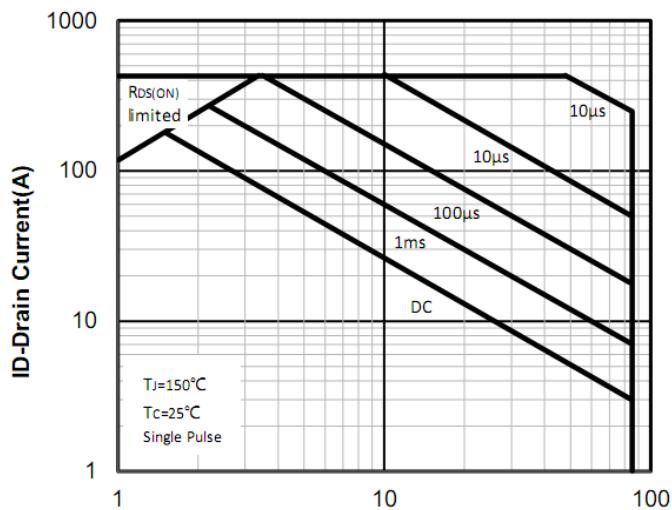
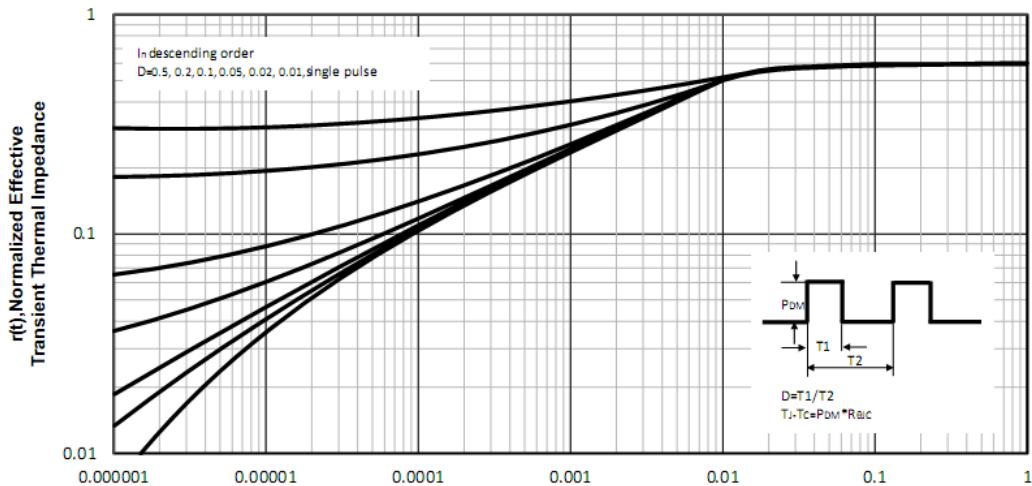
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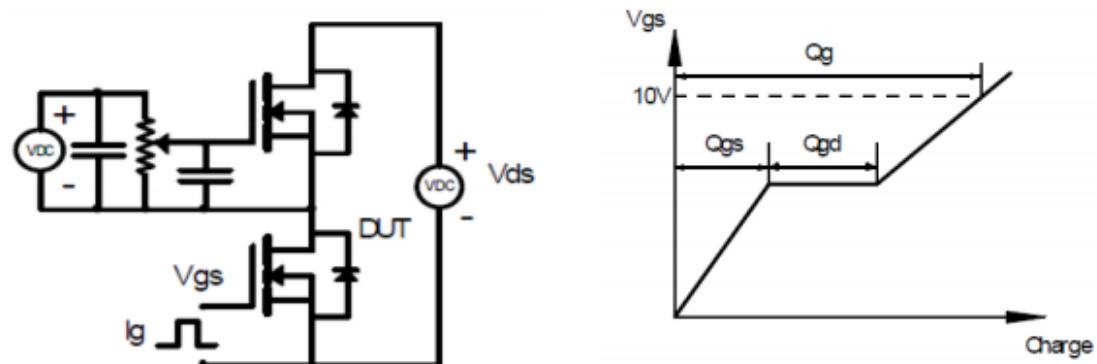
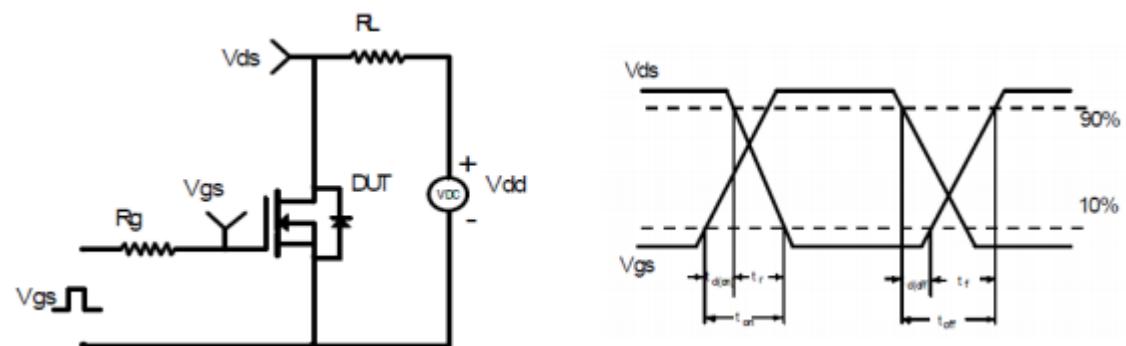
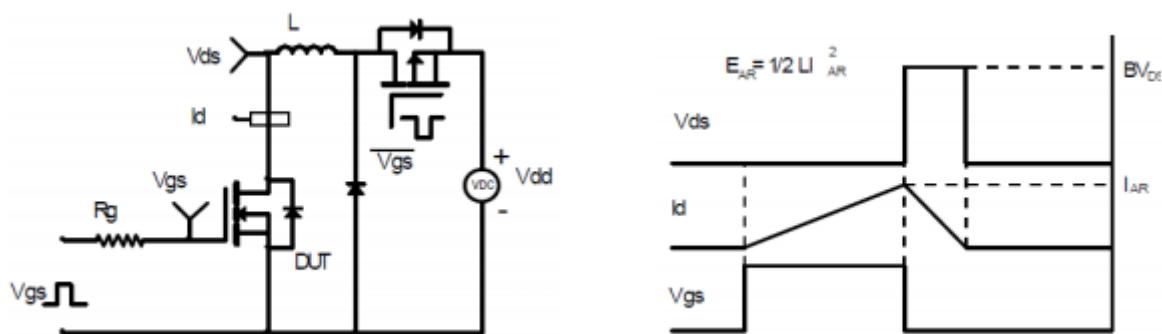
Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
$V_{(BR)DSS}$	Drain- Source Breakdown Voltage	$VGS=0V$ $ID=250\mu A$	85	--	--	V
I_{DSS}	Zero Gate Voltage Drain current	$VDS=80V$, $VGS=0V$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$VGS=\pm 20V$, $VDS=0V$	--	--	± 100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$VDS=VGS$, $ID=250\mu A$	2.0	3.0	4.0	V
$R_{DS(ON)}$	Drain-Source On-State Resistance (Note4)	$VGS=10V$, $ID=50A$	--	4.1	5.2	$m\Omega$
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated) (Note5)						
C_{iss}	Input Capacitance	$VDS=40V$, $VGS=0V$, $F=1MHz$	--	2924	--	pF
C_{oss}	Output Capacitance		--	771	--	pF
C_{rss}	Reverse Transfer Capacitance		--	15	--	pF
Q_g	Total Gate Charge	$VDS=40V$, $ID=50A$,	--	48	--	nC
Q_{gs}	Gate-Source Charge		--	13	--	nC
Q_{gd}	Gate-Drain Charge		--	19	--	nC
Switching Characteristics (Note5)						
$t_{d(on)}$	Turn-on Delay Time	$VDD=40V$, $ID=50A$,	--	19	--	nS
t_r	Turn-on Rise Time		--	33	--	nS
$t_{d(off)}$	Turn-off Delay Time		--	29	--	nS
t_f	Turn-off Fall Time		--	19	--	nS
Source- Drain Diode Characteristics@ TJ = 25°C (unless otherwise stated)						
V_{SD}	Forward on voltage	$IS=50A$, $VGS=0V$	--	--	1.2	V
t_{rr}	Reverse Recovery Time	$IS=50A$, $VGS=0V$, $dI/dt=100A/us$	--	52	--	nS
Q_{rr}	Reverse Recovery Charge		--	73	--	nC

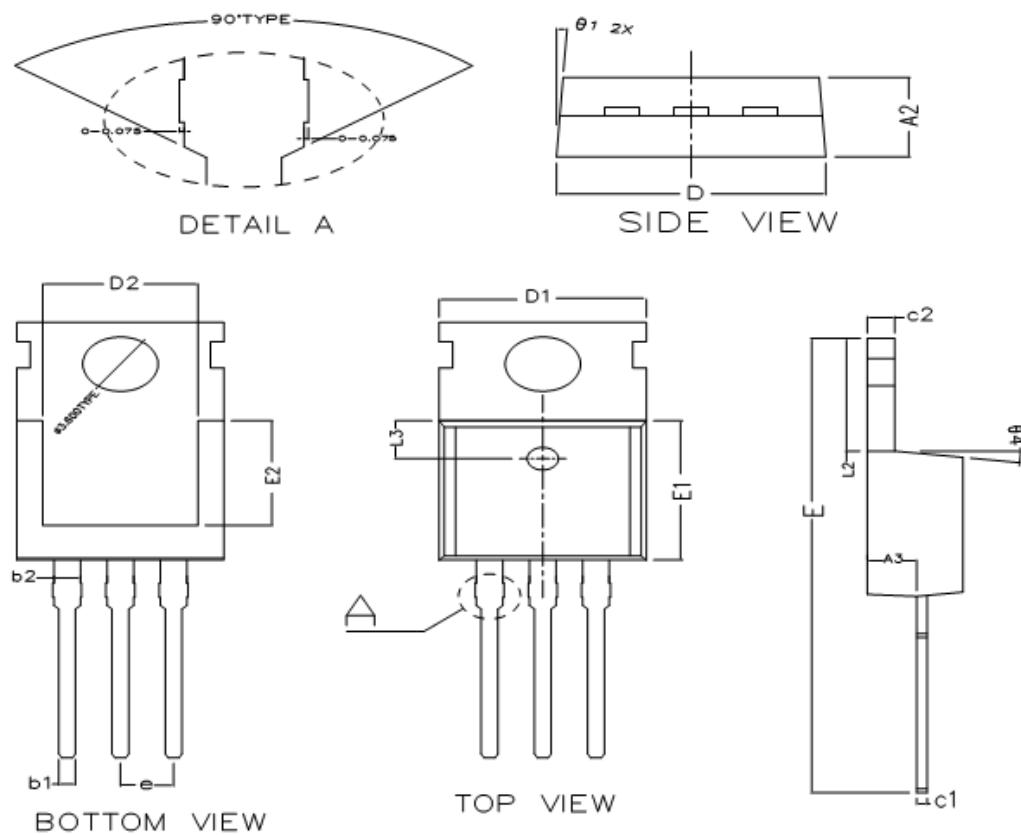
Note:

1. Limited by TJmax, starting TJ = 25° C, RG = 25Ω, VD = 50V, VGS = 10V. Part not recommended for use above this value.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. Surface Mounted on FR4 Board, t ≤ 10 sec.
4. Pulse Test: pulse width ≤ 300 us, duty cycle ≤ 2%.
5. Guranteed by design, not subject to production testing.

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Typical Characteristics

Figure1: T_J Junction Temperature (°C)

Figure2: I_D Drain Current (A)

Figure3: T_J Junction Temperature (°C)

Figure4: V_{DS} Drain-Source Voltage (V)

Figure5: V_{DS} Drain-Source Voltage (V)

Figure6: Q_g Gate Charge (nC)

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Figure7: Vsd Source-Drain Voltage (V)

Figure8: Vgs Gate-Source Voltage (V)

Figure9: VDS Drain -Source Voltage (V)

Figure10: Square Wave Pulse Duration (sec)

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Test Circuit and Waveform:

Figure A Gate Charge Test Circuit & Waveforms

Figure B Switching Test Circuit & Waveforms

Figure C Unclamped Inductive Switching Circuit & Waveforms

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TO-220 Package Outline Dimensions (Units: mm)

**COMMON DIMENSIONS
(UNITS OF MEASURE IS mm)**

	MIN	NORMAL	MAX
A2	4.470	4.570	4.670
A3	2.300	2.350	2.400
b1	0.750	0.800	0.850
b2	1.27 TYPE		
c1	0.450	0.500	0.550
c2	1.250	1.300	1.380
D	9.900	10.000	10.100
D1	10.000TYPE		
D2	8.000TYPE		
E	28.660	28.860	29.060
E1	9.000	9.100	9.200
E2	7.000TYPE		
e	2.540TYPE		
L2	6.350	6.500	6.650
L3	2.50TYPE		
θ_1	3° TYPE		
θ_2	3° TYPE		
θ_3	7° TYPE		
θ_4	7° TYPE		