

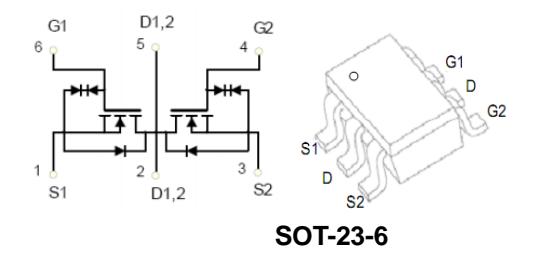
20V/7A Dual N-Channel Enhancement Mode MOSFET
Features

- New technology for high voltage device.
- Low on-resistance and low conduction losses
- Small package
- Ultra Low Gate Charge cause lower driving requirements

BVDSS	20	V
ID	7	A
RDSON@VGS=4.5V	12	mΩ
RDSON@VGS=2.5V	17	mΩ
RDSON@VGS=1.8V	28	mΩ

Applications

- Ideal for Li ion battery pack applications


Order Information

Product	Package	Marking	Reel Size	Reel	Carton
PT8810	SOT-23-6	PT8810	7inch	3000PCS	180000PCS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ratings (TC=25°C Unless Otherwise Noted)				
V _{(BR)DSS}	Drain-Source Breakdown Voltage	20	V	
V _{GS}	Gate-Source Voltage	±12	V	
T _J	Maximum Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	-55 to 150	°C	
I _S	Diode Continuous Forward Current	TA =25°C	7	A

Mounted on Large Heat Sink

I _{DM}	Pulse Drain Current Tested (Sillicon Limit) (Note1)	TA =25°C	30	A
I _D	Continuous Drain current	TA =25°C	7	A
P _D	Maximum Power Dissipation	TA =25°C	0.6	W
R _{θJA}	Thermal Resistance Junction-to-Ambient (Note2)		208.3	°C/W

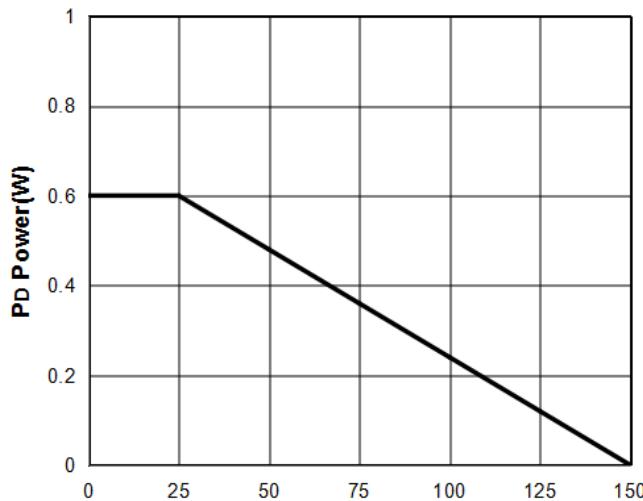
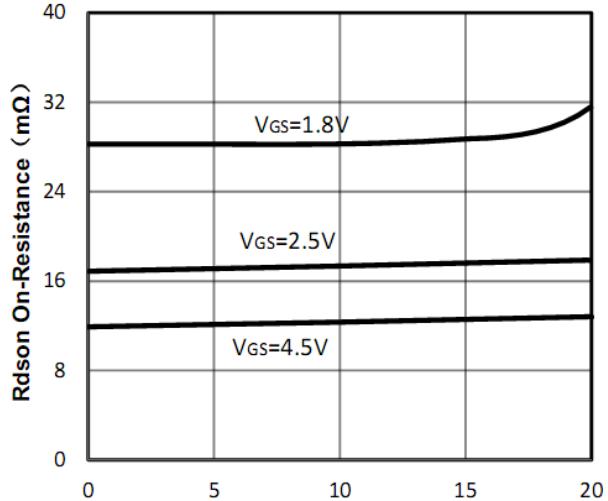
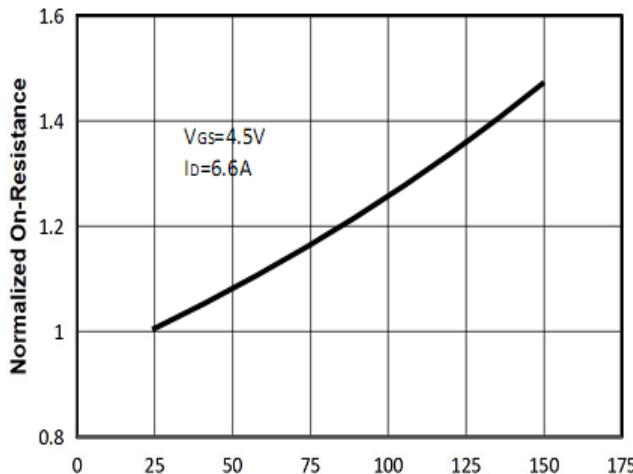
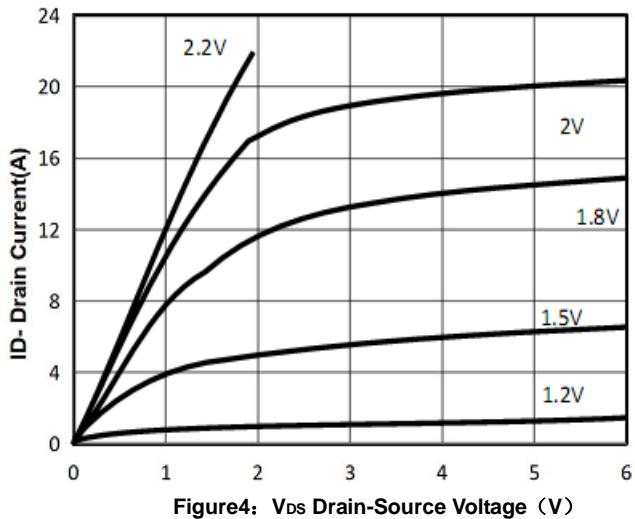
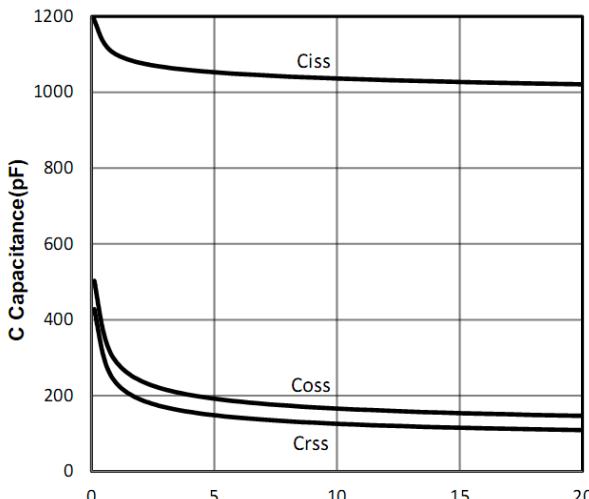
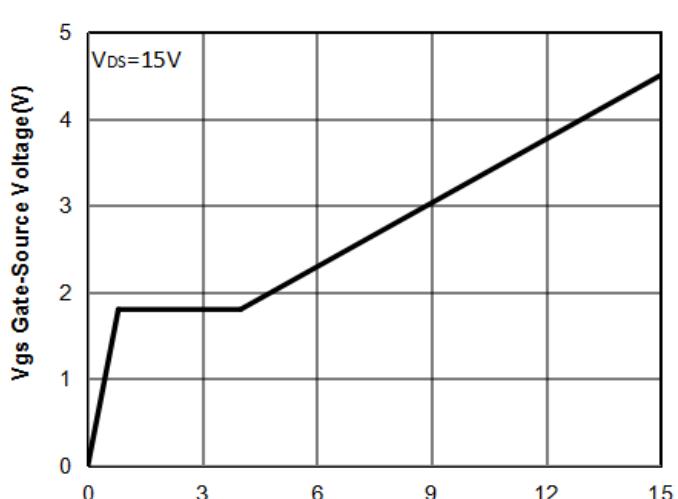


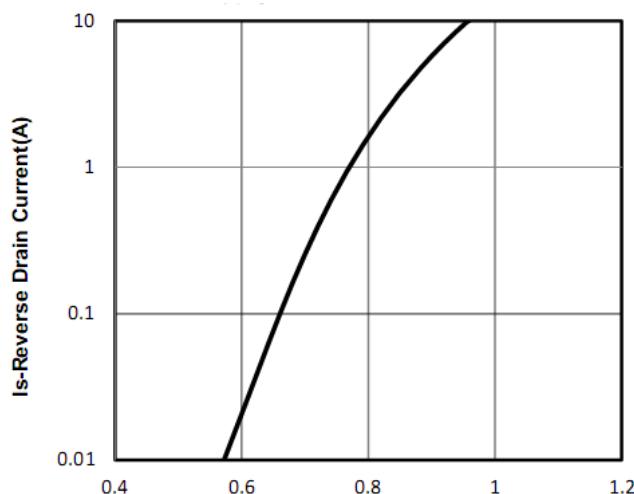
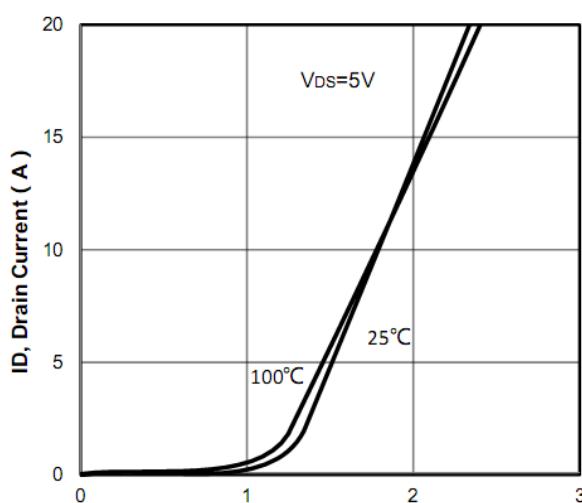
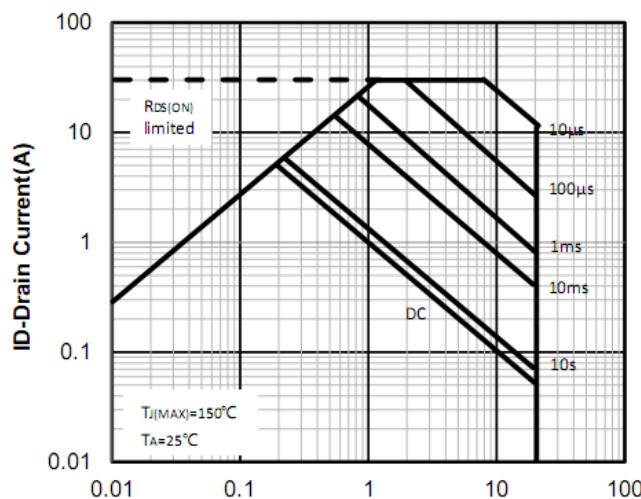
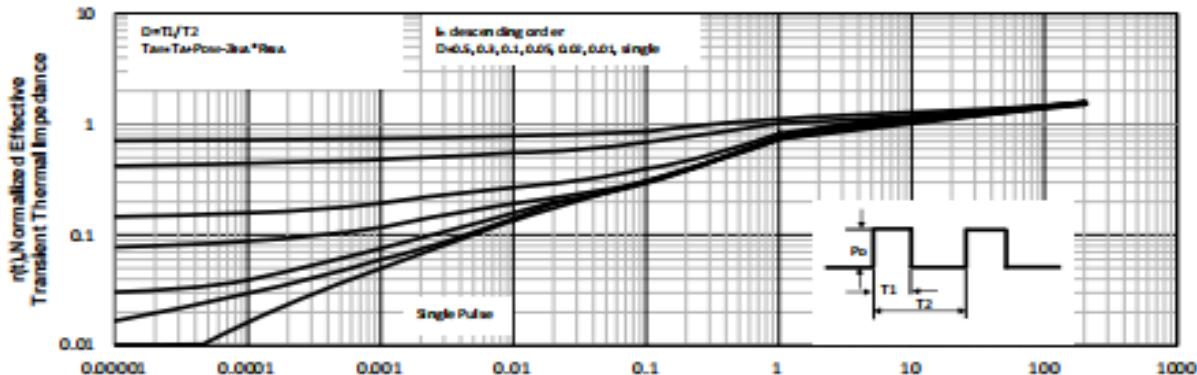
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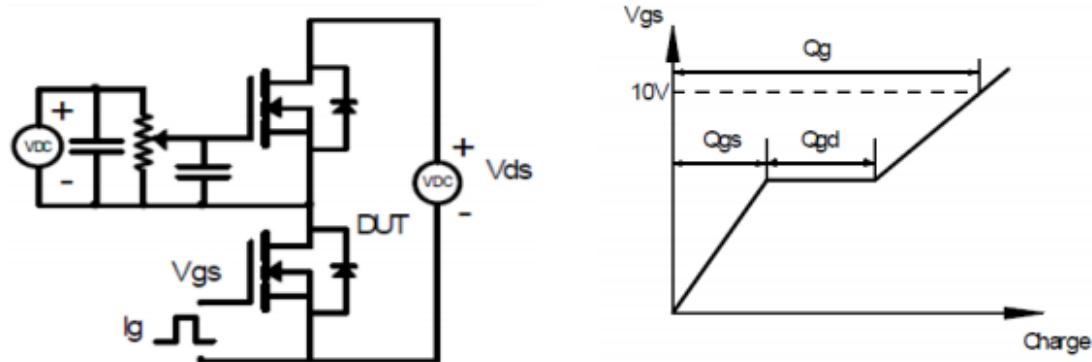
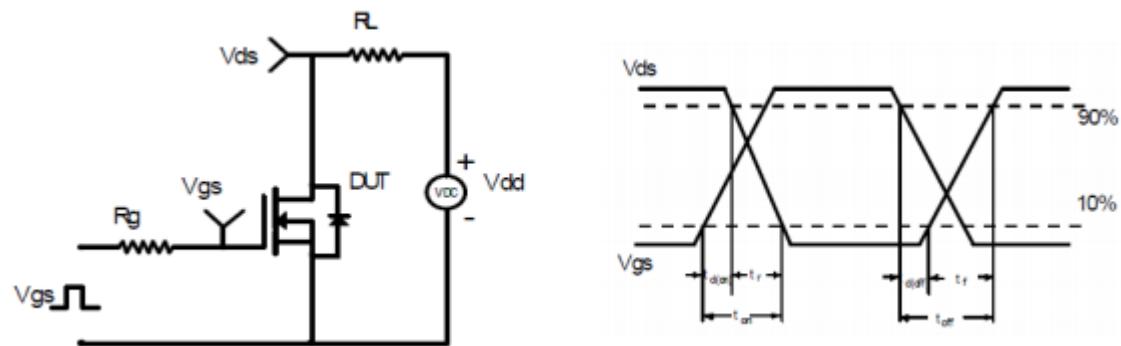
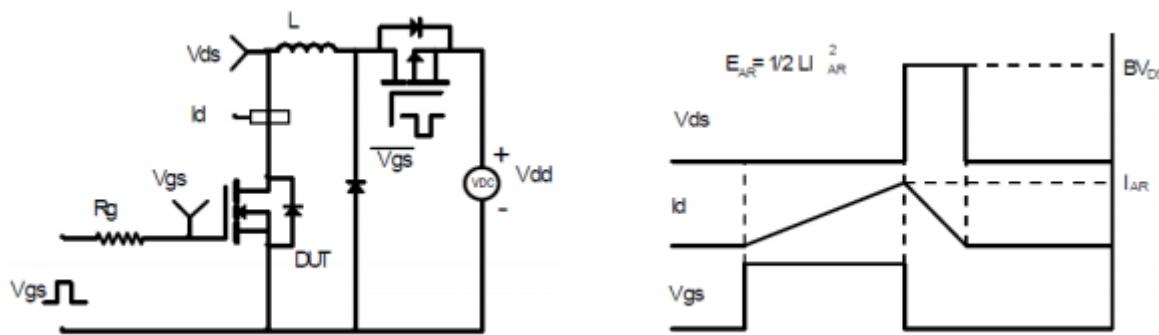
Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_J = 25^\circ C$ (unless otherwise stated)						
$V_{(BR)DSS}$	Drain- Source Breakdown Voltage	$V_{GS}=0V, ID=250\mu A$	20	--	--	V
I_{DSS}	Zero Gate Voltage Drain current	$V_{DS}=16V, V_{GS}=0V$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 8V, V_{DS}=0V$	--	--	± 10	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, ID=250\mu A$	0.4	0.77	1.0	V
$R_{DS(ON)}$	Drain-Source On-State Resistance (Note3)	$V_{GS}=4.5V, ID=6.6A$	--	12	22	mΩ
		$V_{GS}=2.5V, ID=5.5A$	--	17	26	
		$V_{GS}=1.8V, ID=5A$	--	28	35	
Dynamic Electrical Characteristics @ $T_J = 25^\circ C$ (unless otherwise stated) (Note4)						
C_{iss}	Input Capacitance	$V_{DS}=10V,$ $V_{GS}=0V,$ $F=1MHz$	--	1150	--	pF
C_{oss}	Output Capacitance		--	185	--	pF
C_{rss}	Reverse Transfer Capacitance		--	145	--	pF
Q_g	Total Gate Charge	$V_{DS}=10V,$ $ID=7A,$ $V_{GS}=4.5V$	--	15	--	nC
Q_{gs}	Gate-Source Charge		--	0.8	--	nC
Q_{gd}	Gate-Drain Charge		--	3.2	--	nC
Switching Characteristics (Note4)						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=10V,$ $RL=1.35\Omega,$ $V_{GS}=5V$	--	6	--	nS
t_r	Turn-on Rise Time		--	13	--	nS
$t_{d(off)}$	Turn-off Delay Time		--	52	--	nS
t_f	Turn-off Fall Time		--	16	--	nS
Source- Drain Diode Characteristics@ $T_J = 25^\circ C$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$IS=1A, V_{GS}=0V$	--	--	1	V

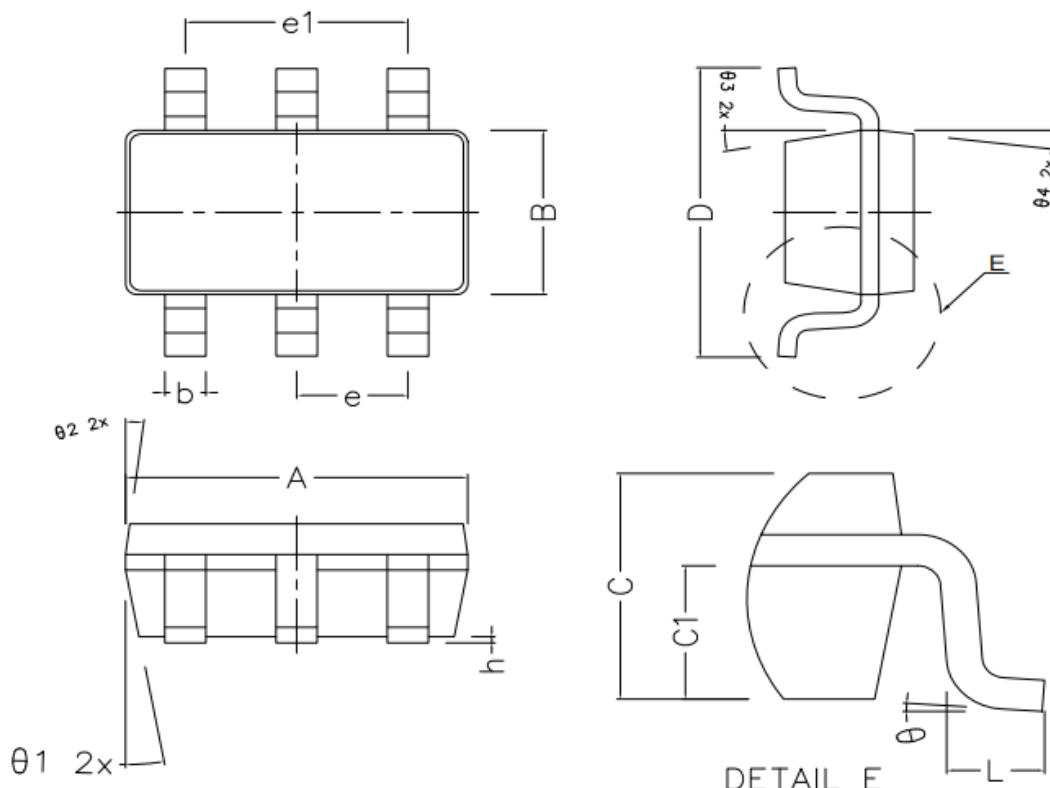
Note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: pulse width ≤ 300 us, duty cycle $\leq 2\%$.
4. Guranteed by design, not subject to production testing.

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Typical Characteristics

Figure1: TJ Junction Temperature (°C)

Figure2: Id Drain Current (A)

Figure3: TJ Junction Temperature (°C)

Figure4: Vds Drain-Source Voltage (V)

Figure5: Vds Drain-Source Voltage (V)

Figure6: Qg Gate Charge (nC)

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Figure7: V_{SD} Source-Drain Voltage (V)

Figure8: V_{GS} Gate-Source Voltage (V)

Figure9: V_{DS} Drain -Source Voltage (V)

Figure10: Square Wave Pulse Duration (sec)

20V/7A Dual N-Channel Enhancement Mode MOSFET
Test Circuit and Waveform:

Figure A Gate Charge Test Circuit & Waveforms

Figure B Switching Test Circuit & Waveforms

Figure C Unclamped Inductive Switching Circuit & Waveforms

20V/7A Dual N-Channel Enhancement Mode MOSFET
SOT-23-6L Package Outline Dimensions (Units: mm)


COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A	2.820	2.920	3.020
B	1.500	1.600	1.700
C	1.050	1.100	1.150
C1	0.600	0.650	0.700
D	2.650	2.800	2.950
L	0.300	0.450	0.600
b	0.280	0.350	0.420
h	0.020	0.050	0.100
K	0.120	-	0.230
e	0.950TYPE		
e1	1.900TYPE		
θ ₁	10° TYPE		
θ ₂	7° TYPE		
θ ₃	10° TYPE		
θ ₄	7° TYPE		
θ	0° ~ 8°		