

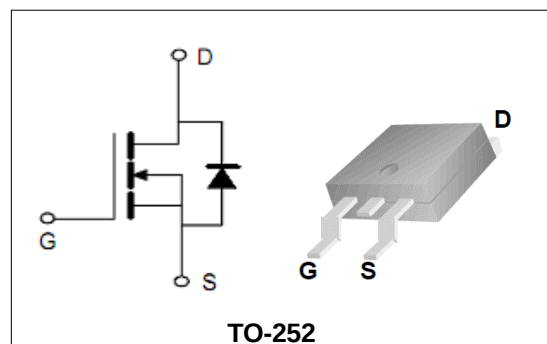
**40V/80A N-Channel Advanced Power MOSFET****Features**

- Improved dv/dt Capability, High Ruggedness.
- Maximum Junction Temperature Range (150°C)
- 100% Avalanche Tested

BVDSS	40	V
ID	80	A
RDSON@VGS=10V	4.7	mΩ
RDSON@VGS=4.5V	6.5	mΩ

Applications

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)

**TO-252****Order Information**

Product	Package	Marking	Reel Size	Reel	Carton
PTD4080	TO-252	PTD4080	13inch	2500PCS	50000PCS

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (TC=25°C Unless Otherwise Noted)				
V _{(BR)DSS}	Drain-Source Breakdown Voltage		40	V
V _{GS}	Gate-Source Voltage		±20	V
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current	TC =25°C	80	A
Mounted on Large Heat Sink				
E _{AS}	Single Pulse Avalanche Energy (Note1)		240	mJ
I _{DM}	Pulse Drain Current Tested (Silicon Limit) (Note2)	TC =25°C	320	A
I _D	Continuous Drain current	TC =25°C	80	A
P _D	Maximum Power Dissipation	TC =25°C	58	W
R _{θJC}	Thermal Resistance Junction-to-Case (Note3)		1.98	°C/W



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Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
V _{(BR)DSS}	Drain- Source Breakdown Voltage	VGS=0V ID=250μA	40	--	--	V
I _{DSS}	Zero Gate Voltage Drain current	VDS=32V,VGS=0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
V _{GS(TH)}	Gate Threshold Voltage	VDS=VGS,ID=250μA	1	1.6	2.5	V
R _{DS(ON)}	Drain-Source On-State Resistance (Note4)	VGS=10V, ID=40A	--	4.7	7	mΩ
		VGS=4.5V, ID=10A	--	6.5	8	mΩ
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated) (Note5)						
C _{iss}	Input Capacitance	VDS=20V, VGS=0V, F=1MHz	--	1400	--	pF
C _{oss}	Output Capacitance		--	190	--	pF
C _{rss}	Reverse Transfer Capacitance		--	150	--	pF
Q _g	Total Gate Charge	VDS=20V, ID=20A, VGS=10V	--	37	--	nC
Q _{gs}	Gate-Source Charge		--	7	--	nC
Q _{gd}	Gate-Drain Charge		--	18	--	nC
Switching Characteristics (Note5)						
t _{d(on)}	Turn-on Delay Time	VDD=20V, ID=10A, VGS=10V, RG=4.7Ω	--	16	--	nS
t _r	Turn-on Rise Time		--	15	--	nS
t _{d(off)}	Turn-off Delay Time		--	20	--	nS
t _f	Turn-off Fall Time		--	12	--	nS
Source- Drain Diode Characteristics@ TJ = 25°C (unless otherwise stated)						
V _{SD}	Forward on voltage	IS=20A,VGS=0V	--	--	1.2	V

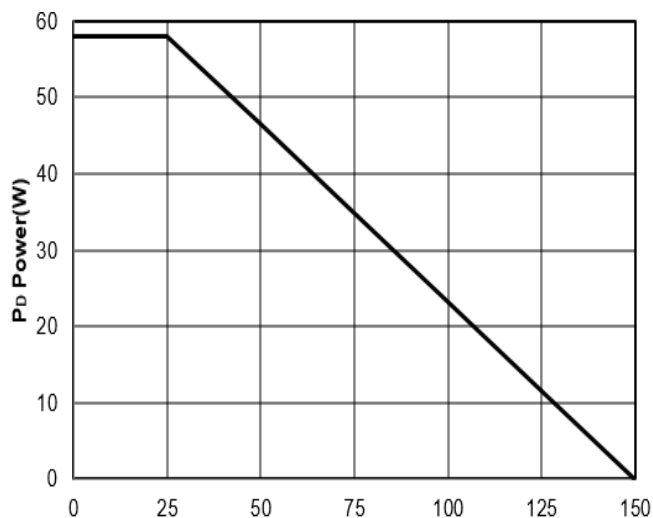
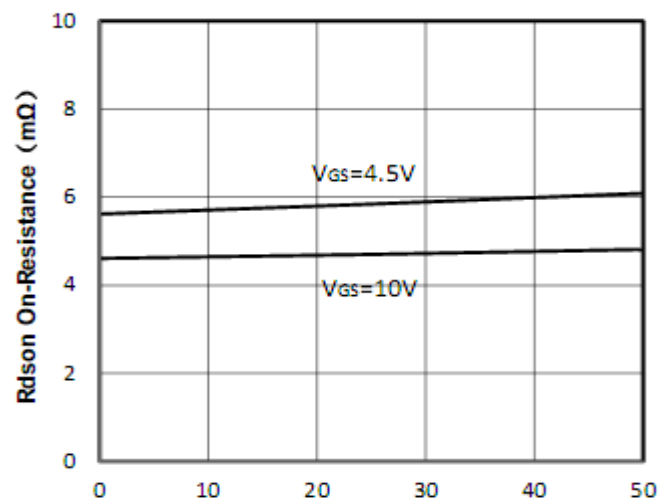
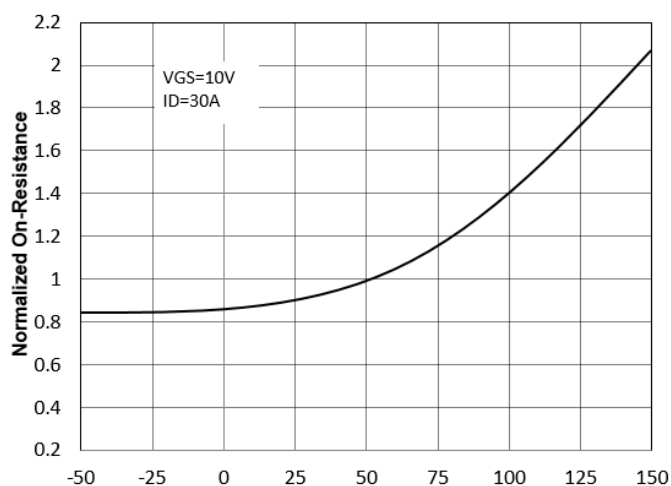
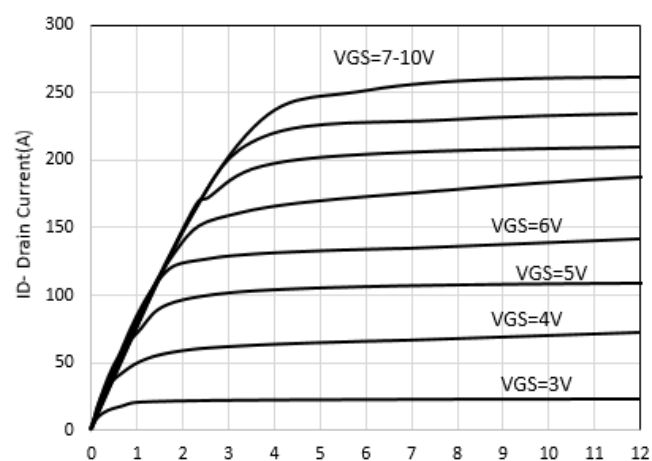
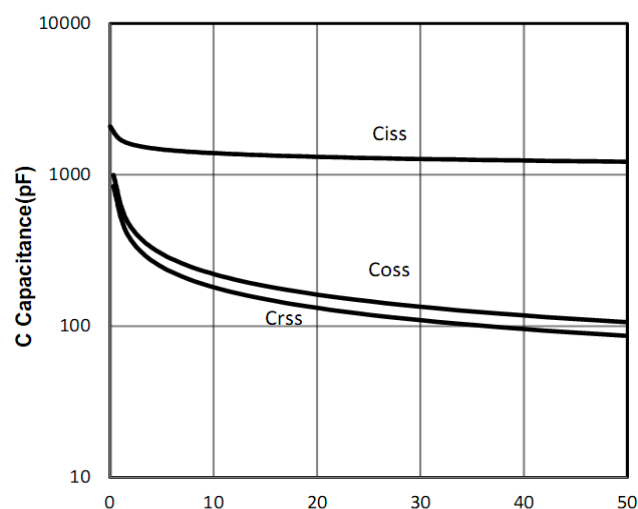
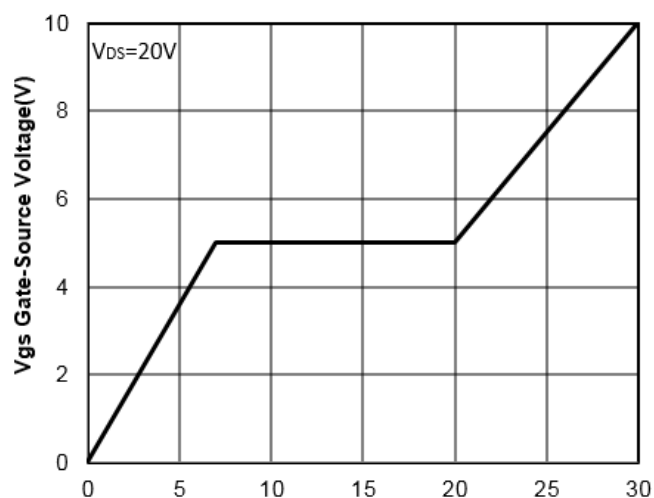
Note:

- Limited by TJmax, starting TJ = 25° C, RG = 25Ω, VD =20V, VGS =10V. Part not recommended for use above this value.
- Repetitive Rating: Pulse width limited by maximum junction temperature.
- Surface Mounted on FR4 Board, t ≤ 10 sec.
- Pulse Test: pulse width ≤ 300 us, duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.



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Typical Characteristics

Figure1: T_j Junction Temperature (°C)Figure2: I_d Drain Current (A)Figure3: T_j Junction Temperature (°C)Figure4: V_{ds} Drain-Source Voltage (V)Figure5: V_{ds} Drain-Source Voltage (V)Figure6: Q_g Gate Charge (nC)



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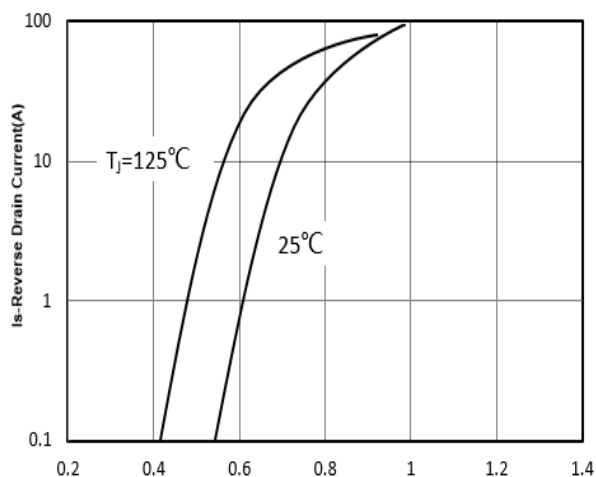


Figure7: Vsd Source-Drain Voltage (V)

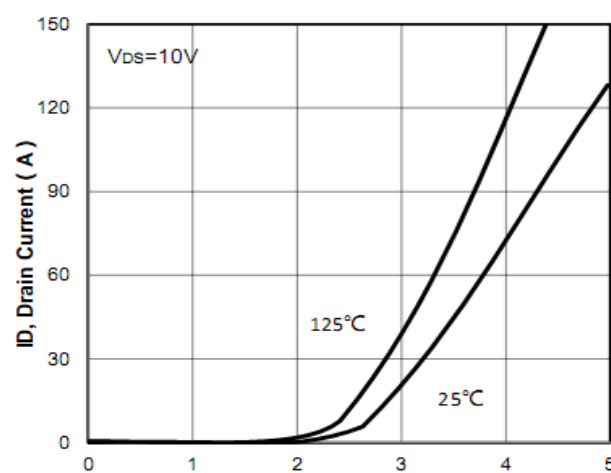


Figure8: Vgs Gate-Source Voltage (V)

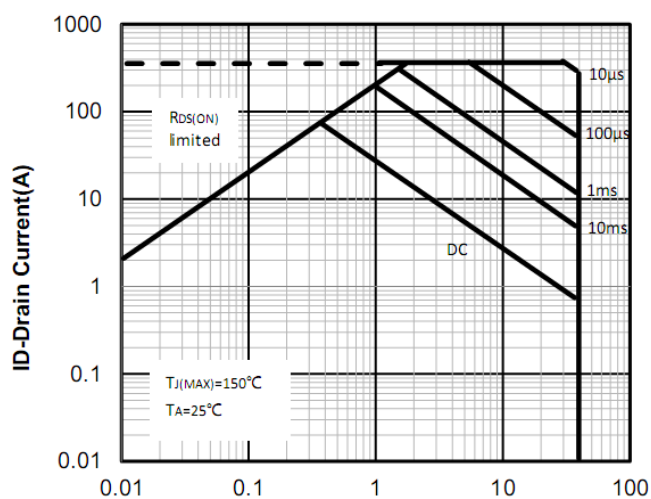


Figure9: Vds Drain -Source Voltage (V)

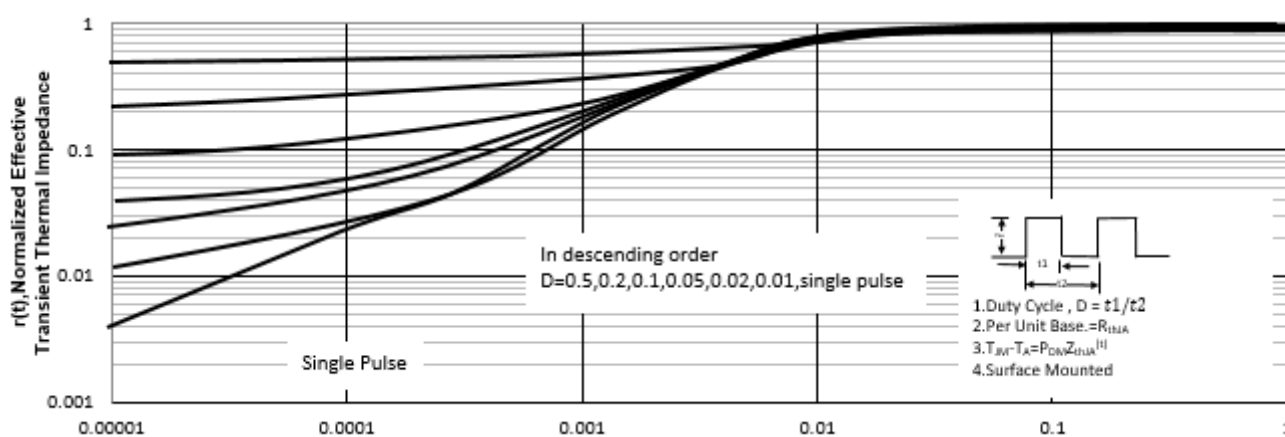


Figure10: Square Wave Pulse Duration (sec)



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Test Circuit and Waveform:

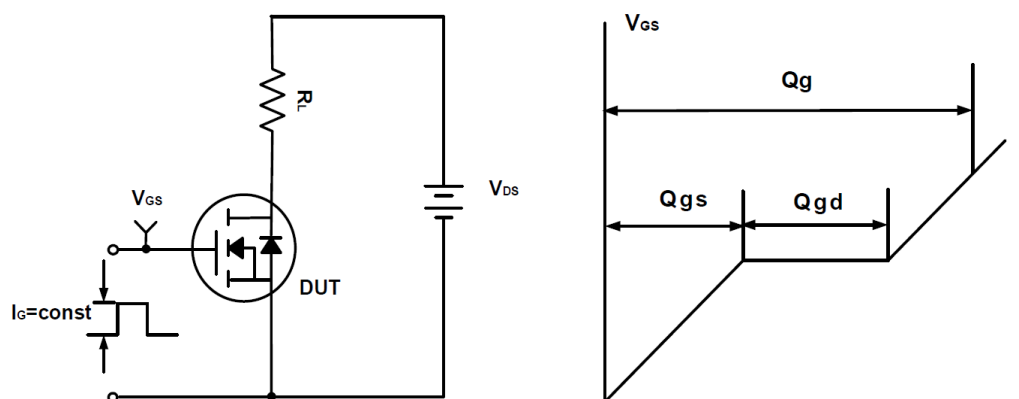


Figure A Gate Charge Test Circuit & Waveforms

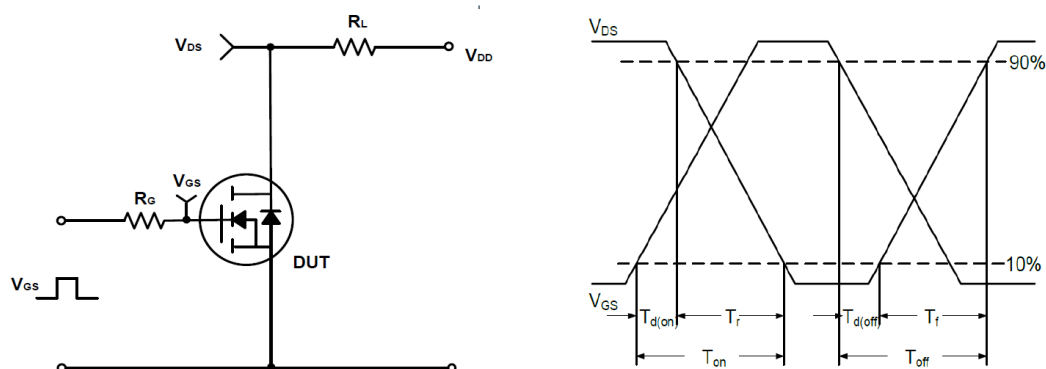


Figure B Switching Test Circuit & Waveforms

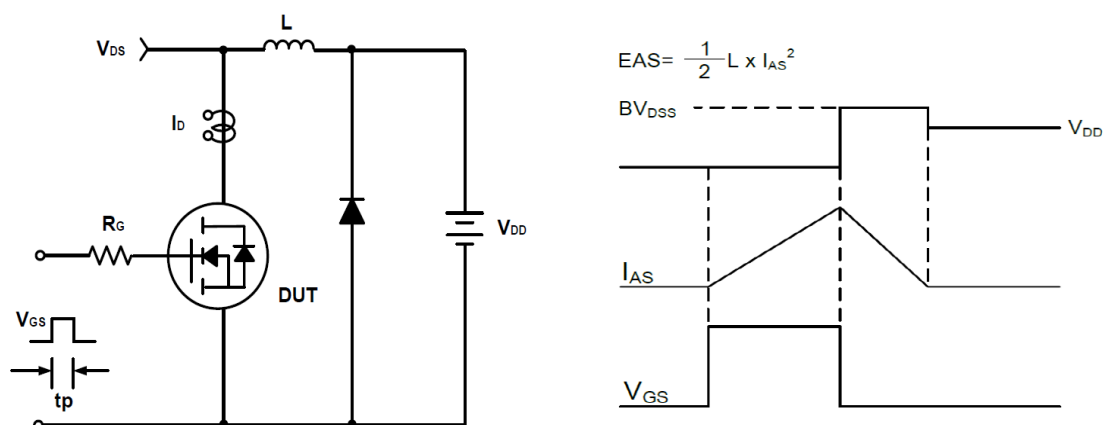
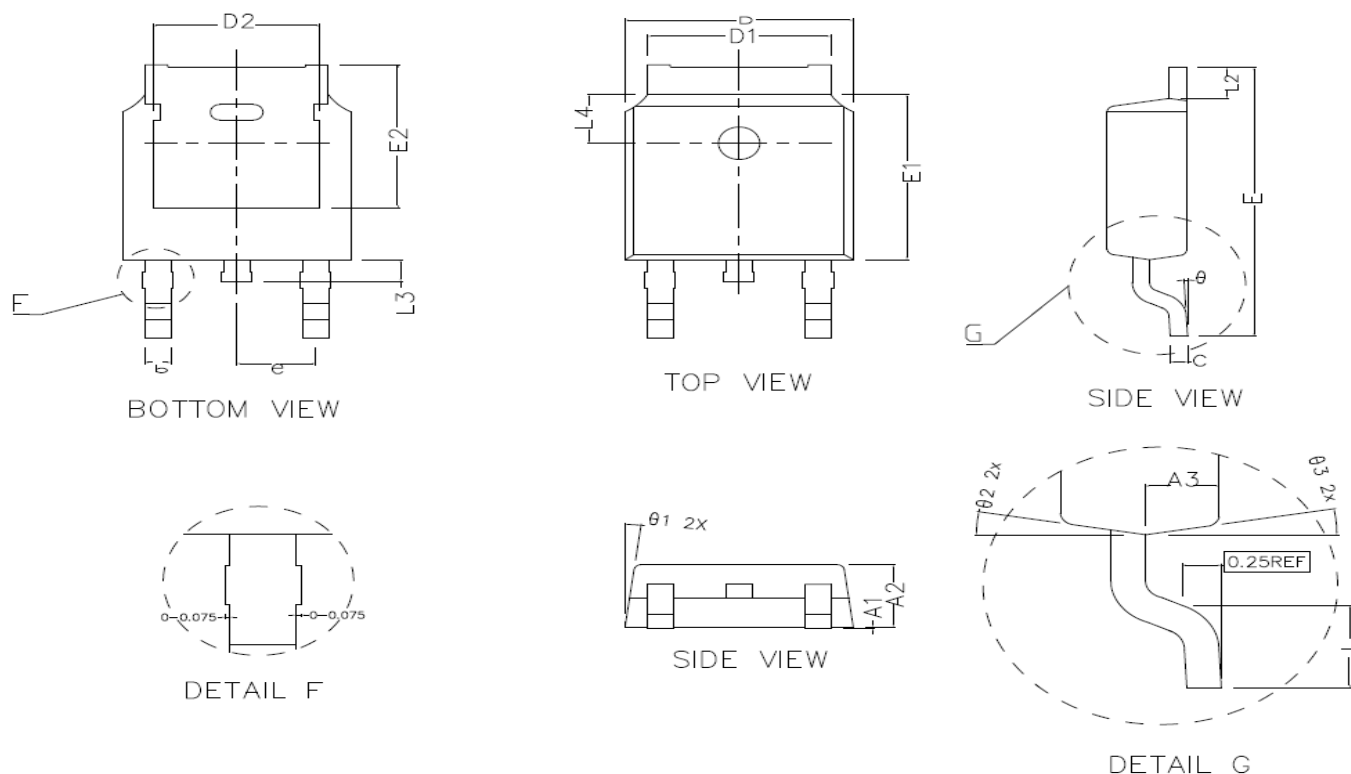


Figure C Unclamped Inductive Switching Circuit & Waveforms

**40V/80A N-Channel Advanced Power MOSFET****TO-252 Package Outline Dimensions (Units: mm)**

COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A1	0.000	0.100	0.150
A2	2.200	2.300	2.400
A3	1.020	1.070	1.120
b	0.710	0.760	0.810
c	0.460	0.508	0.550
D	6.500	6.600	6.700
D1	5.330REF		
D2	4.830REF		
E	9.900	10.100	10.300
E1	6.000	6.100	6.200
E2	5.600REF		
e	2.286TYPE		
L	1.400	1.550	1.700
L2	1.10REF		
L3	0.80REF		
L4	1.80REF		
θ	0~8°		
θ_1	7° TYPE		
θ_2	10° TYPE		
θ_3	10° TYPE		