

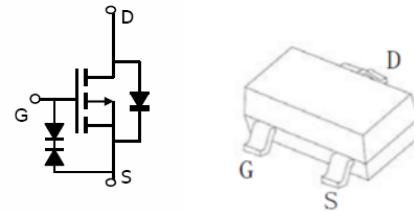
**-60V/-0.13A P-Channel Advanced Power MOSFET****Features**

- Advanced trench process technology
- High Density Cell Design For Ultra Low On-Resistance
- ESD Rating: HBM 2000V

BVDSS	-60	V
ID	-0.13	A
RDSON@VGS=-10V	3	Ω
RDSON@VGS=-5V	5	Ω

Applications

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)



SOT-23

Order Information

Product	Package	Marking	Reel Size	Reel	Carton
BSS84	SOT-23	B84	7inch	3000PCS	180000PCS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ratings (TC=25°C Unless Otherwise Noted)				
V _{(BR)DSS}	Drain-Source Breakdown Voltage	-60	V	
V _{GS}	Gate-Source Voltage	±20	V	
T _J	Maximum Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	-55 to 150	°C	
I _S	Diode Continuous Forward Current	TA =25°C	-0.13	A
Mounted on Large Heat Sink				
I _{DM}	Pulse Drain Current Tested (Silicon Limit) (Note1)	TA =25°C	-0.52	A
I _D	Continuous Drain current	TA =25°C	-0.13	A
P _D	Maximum Power Dissipation	TA =25°C	0.225	W
R _{θJA}	Thermal Resistance Junction-to-Ambient (Note2)		556	°C/W

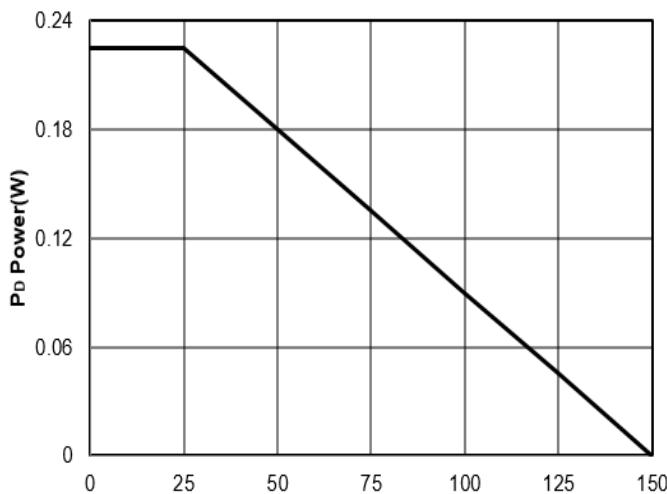
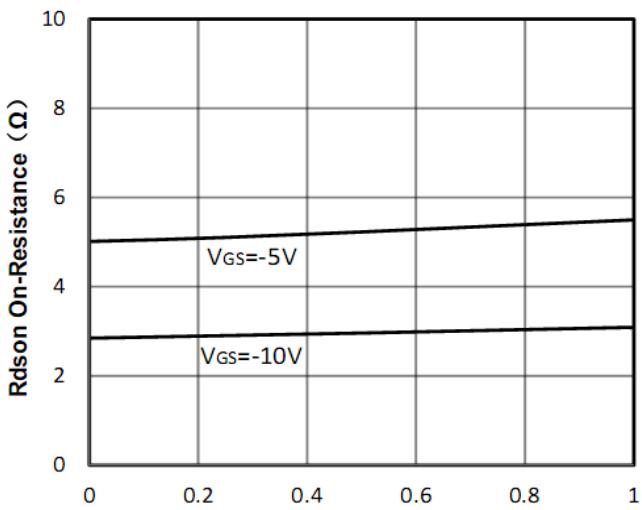
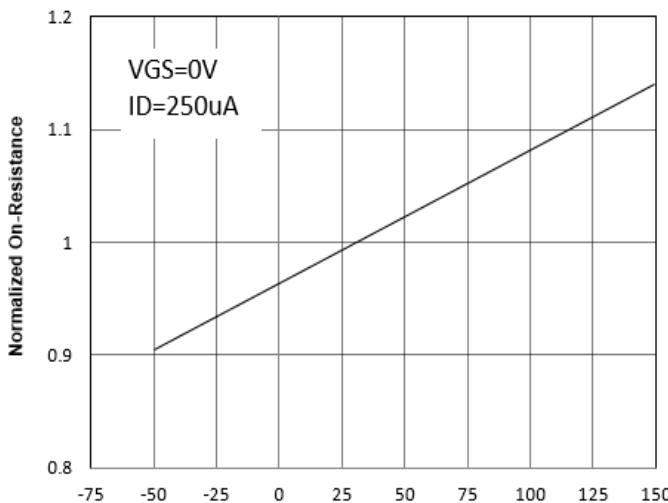
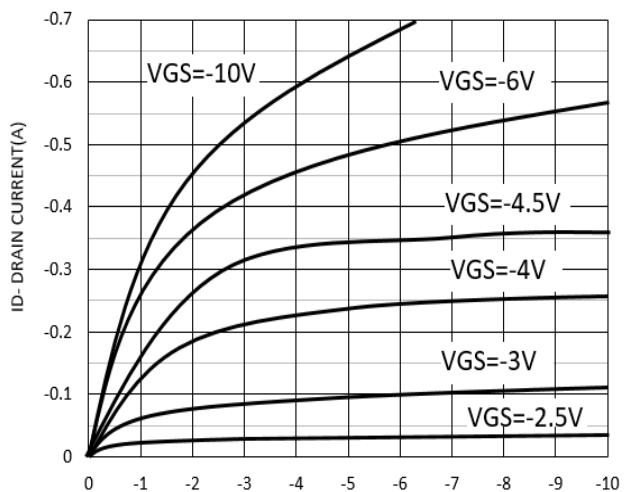
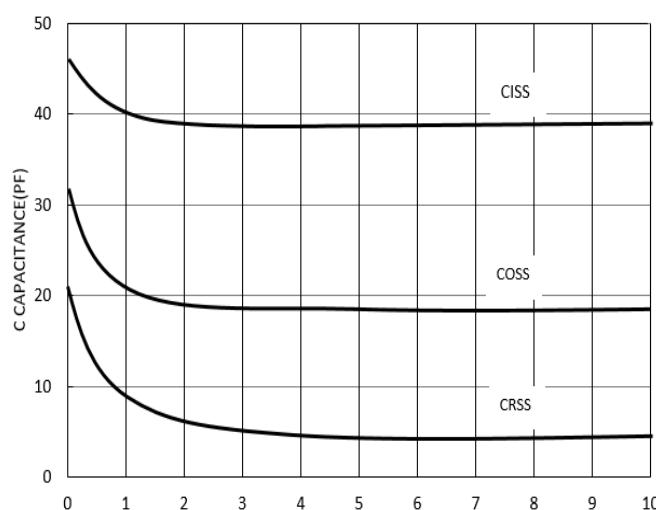
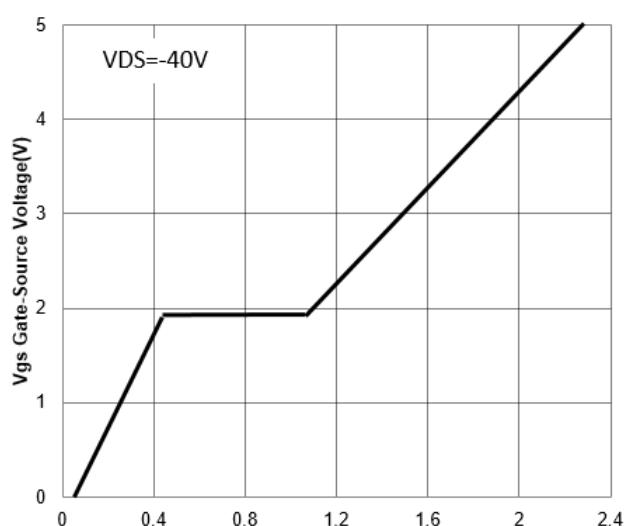


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Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
$V_{(BR)DSS}$	Drain- Source Breakdown Voltage	$VGS=0V, ID=-250\mu A$	-60	--	--	V
I_{DSS}	Zero Gate Voltage Drain current	$VDS=-50V, VGS=0V$	--	--	-1	μA
I_{GSS}	Gate-Body Leakage Current	$VGS=\pm 20V, VDS=0V$	--	--	± 10	μA
$V_{GS(TH)}$	Gate Threshold Voltage	$VDS=VGS, ID=-250\mu A$	-0.8	--	-2	V
$R_{DS(ON)}$	Drain-Source On-State Resistance (Note3)	$VGS=-10V, ID=-0.5A$	--	3	8	Ω
		$VGS=-5V, ID=-0.2A$	--	5	10	Ω
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated) (Note4)						
C_{iss}	Input Capacitance	$VDS=-5V,$ $VGS=0V,$ $F=1MHz$	--	41.7	--	pF
C_{oss}	Output Capacitance		--	19.3	--	pF
C_{rss}	Reverse Transfer Capacitance		--	4	--	pF
Q_g	Total Gate Charge	$VDS=-40V,$ $ID=-0.1A,$ $VGS=-4.5V$	--	2.3	--	nC
Q_{gs}	Gate-Source Charge		--	2.4	--	nC
Q_{gd}	Gate-Drain Charge		--	0.7	--	nC
Switching Characteristics (Note4)						
$t_{d(on)}$	Turn-on Delay Time	$VDS=-15V,$ $ID=-0.25A, RL=6\Omega,$ $RG=50\Omega,$ $VGS=-15V$	--	13.7	--	nS
t_r	Turn-on Rise Time		--	6.2	--	nS
$t_{d(off)}$	Turn-off Delay Time		--	15.9	--	nS
t_f	Turn-off Fall Time		--	2.8	--	nS
Source- Drain Diode Characteristics@ TJ = 25°C (unless otherwise stated)						
V_{SD}	Forward on voltage	$IS=-0.5A, VGS=0V$	--	--	-1.3	V

Note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: pulse width ≤ 300 us, duty cycle $\leq 2\%$.
4. Guranteed by design, not subject to production testing.

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Typical Characteristics

Figure1: TJ Junction Temperature (°C)

Figure2: -Id Drain Current (A)

Figure3: TJ Junction Temperature (°C)

Figure4: -Vds Drain-Source Voltage (A)

Figure5: -Vds Drain-Source Voltage (V)

Figure6: Qg Gate Charge (nC)

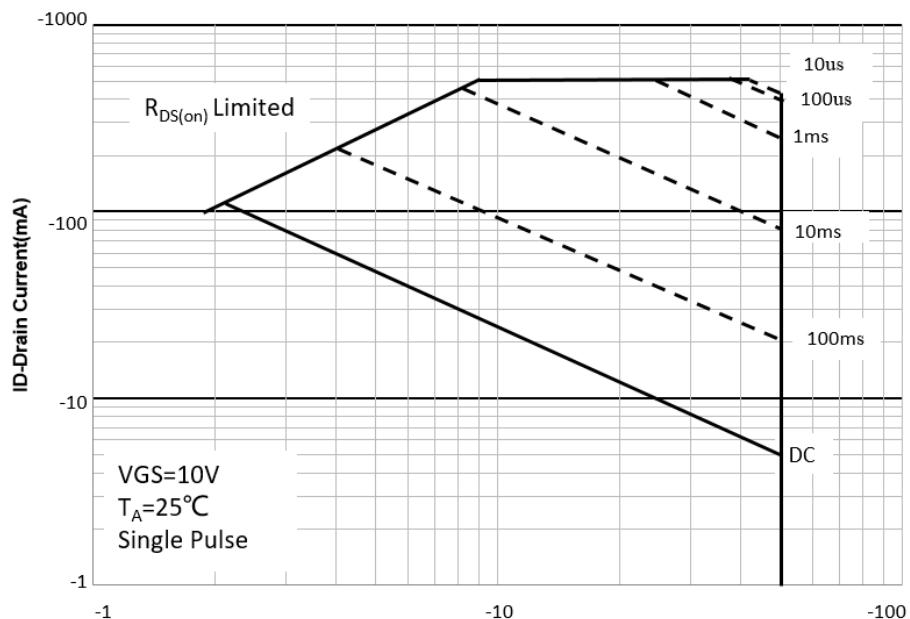
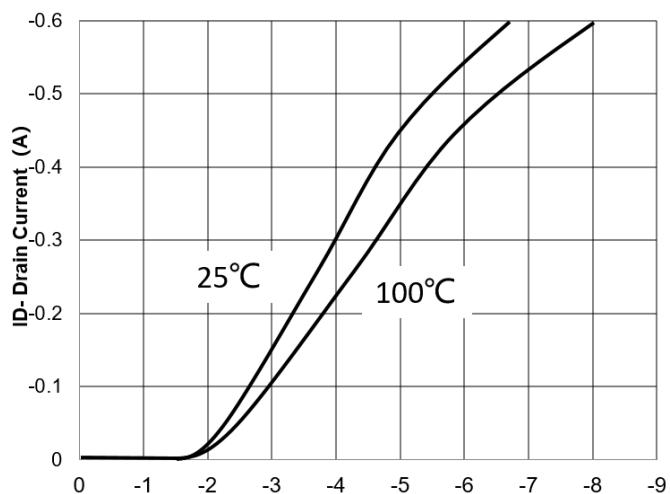
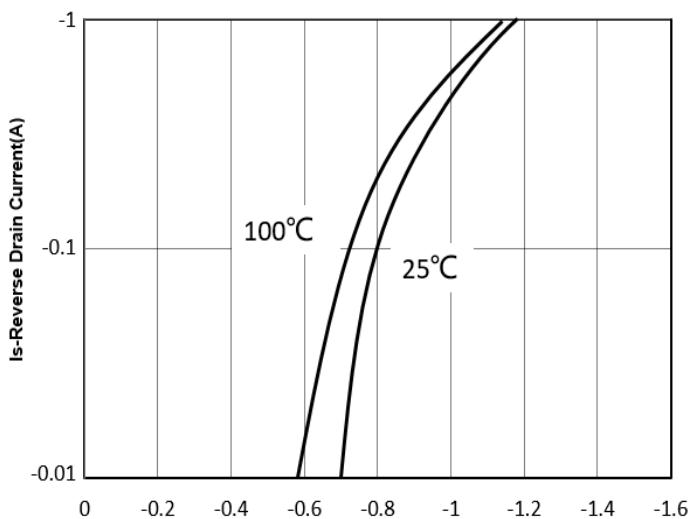
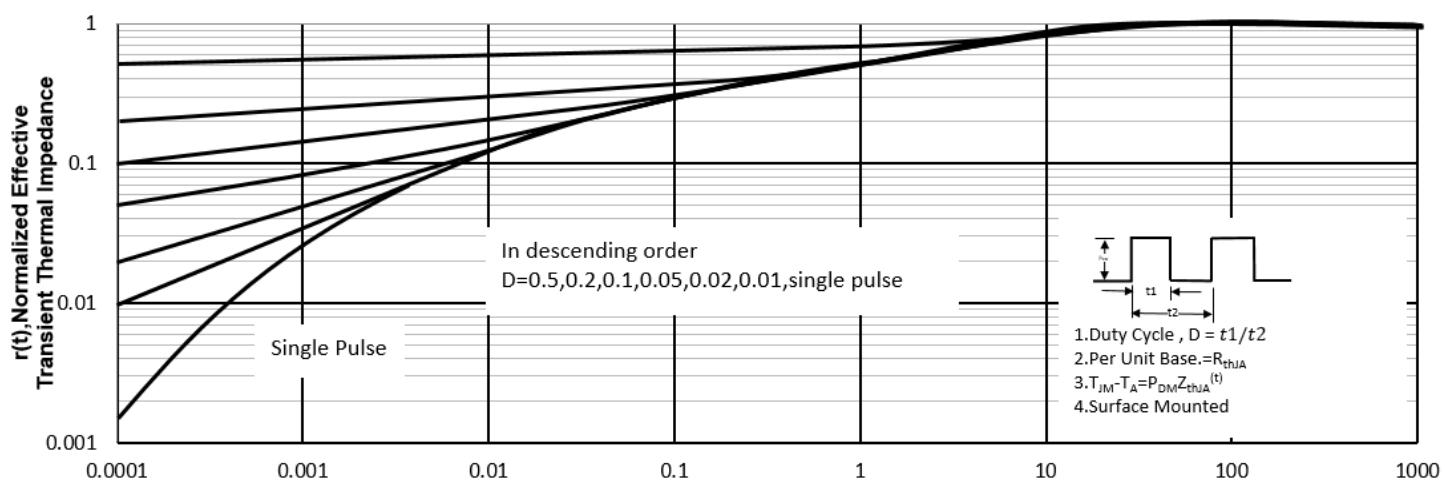
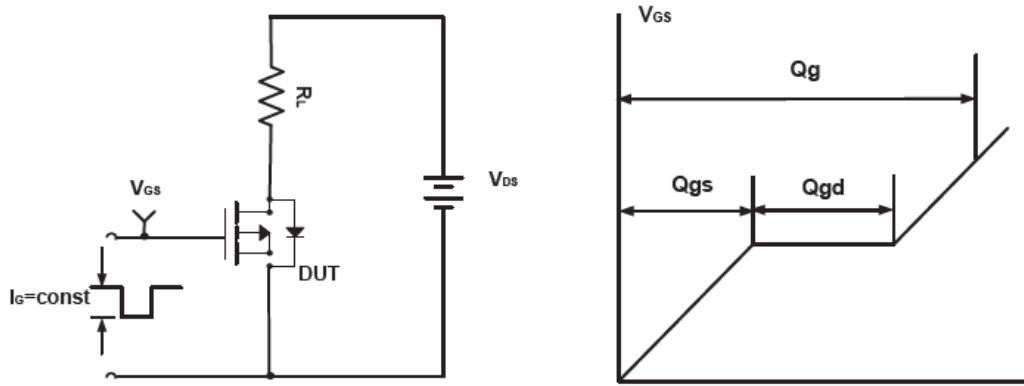
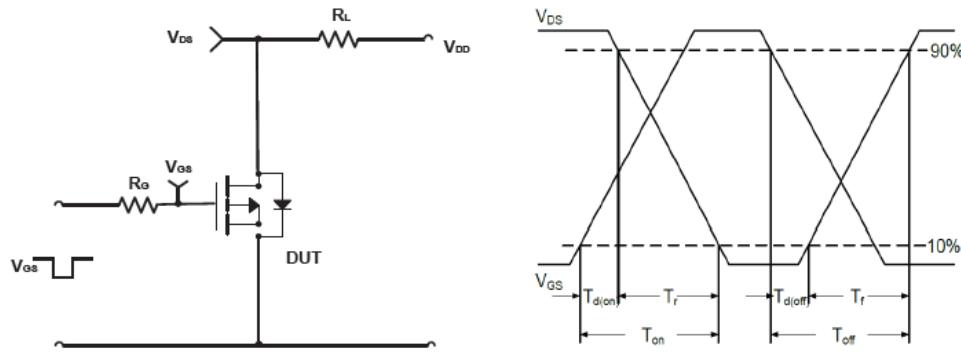
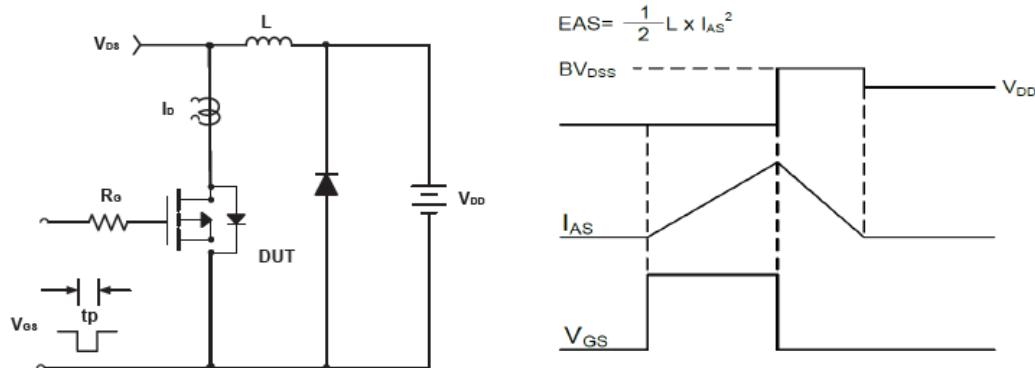
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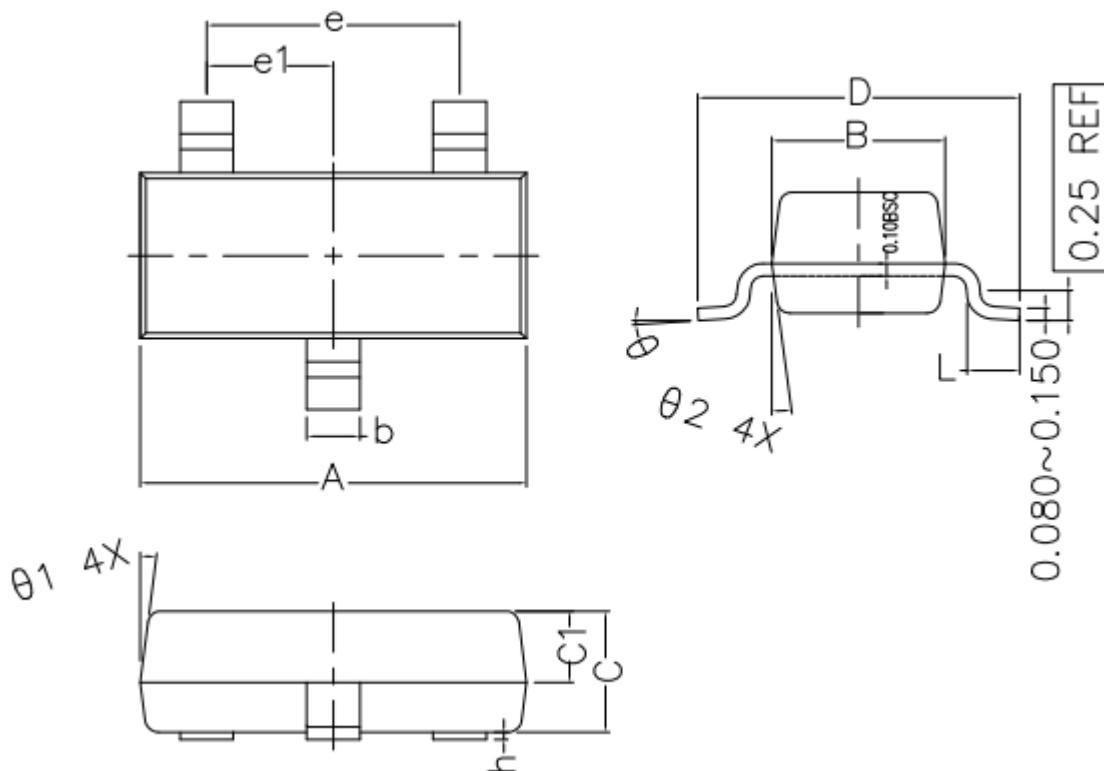
Figure 9: -V_{sd} Drain-Source Voltage (V) vs ID-Drain Current (mA)



-60V/-0.13A P-Channel Advanced Power MOSFET
Test Circuit and Waveform:

Figure A Gate Charge Test Circuit & Waveforms

Figure B Switching Test Circuit & Waveforms

Figure C Unclamped Inductive Switching Circuit & Waveforms

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SOT-23 Package Outline Dimensions (Units: mm)



COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A	2.800	2.900	3.000
B	1.200	1.300	1.400
C	0.900	1.000	1.100
C1	0.500	0.550	0.600
D	2.250	2.400	2.550
L	0.300	0.400	0.500
h	0.010	0.050	0.100
b	0.300	0.400	0.500
e	1.90 TYPE		
e1	0.95 TYPE		
theta_1	7° TYPE		
theta_2	7° TYPE		
theta	0° ~ 7°		